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1 MOTIVATION, PROPOSED CONCEPT AND FRAMEWORK

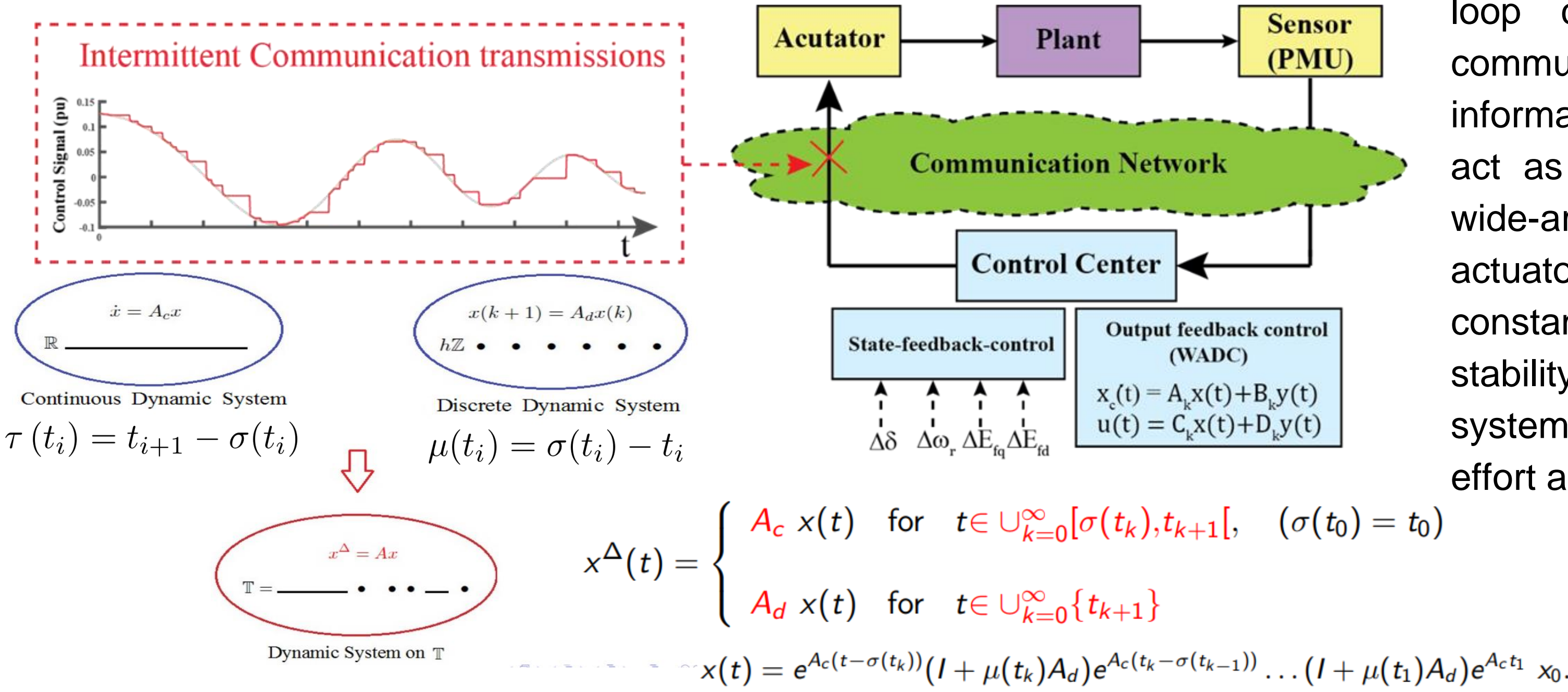


Fig.1 Control block diagram of the feedback loop with connecting physical network through communication network which introduces delays

Modern power grids are relying more on closed-loop controls with signals passed through communication networks. If any interruption of information transmission occurs, the system will act as a discrete time subsystem. Here for a wide-area damping control (WADC) example, an actuator or measurement signal will hold (remain constant) during a random time interval. Our stability condition allows us to better capture the systems hybrid behavior with less computational effort and greater precision.

2 STABILITY CONDITION: EXPONENTIAL ALMOST SURELY REGION

- Switched system with static-feedback control law
- Switched system with dynamic output feedback control law

$$x^\Delta(t) = \begin{cases} (A + BK)x(t), & t \in \bigcup_{i=0}^{\infty} [\sigma(t_i), t_{i+1}) \\ \left(\frac{e^{A\mu(t_i)} - I}{\mu(t_i)} \right) (I + A^{-1}BK) x(t), & t \in \bigcup_{i=0}^{\infty} \{t_{i+1}\} \end{cases}$$

- Stability condition:

$$\mathbb{E}[\Re(\lambda_j)\tau_k] + \mathbb{E}[\log(|1 + \mu_k \lambda_j|)] < 0, \quad \forall 1 \leq j \leq n.$$

$$\hat{x}^\Delta(t) = \begin{cases} \begin{bmatrix} A + BD_k C & BC_k \\ B_k C & A_k \end{bmatrix} \hat{x}(t), & t \in \bigcup_{i=0}^{\infty} [\sigma(t_i), t_{i+1}) \\ \left(\frac{e^{\begin{bmatrix} A & BC_k \\ 0 & A_k \end{bmatrix} \mu(t)} - I}{\mu(t)} \right) \times \\ \begin{bmatrix} I + \begin{bmatrix} A & BC_k \\ 0 & A_k \end{bmatrix}^{-1} \begin{bmatrix} BD_k C & 0 \\ B_k C & 0 \end{bmatrix} & 0 \end{bmatrix} \hat{x}(t), & t \in \bigcup_{i=0}^{\infty} \{t_{i+1}\} \end{cases}$$

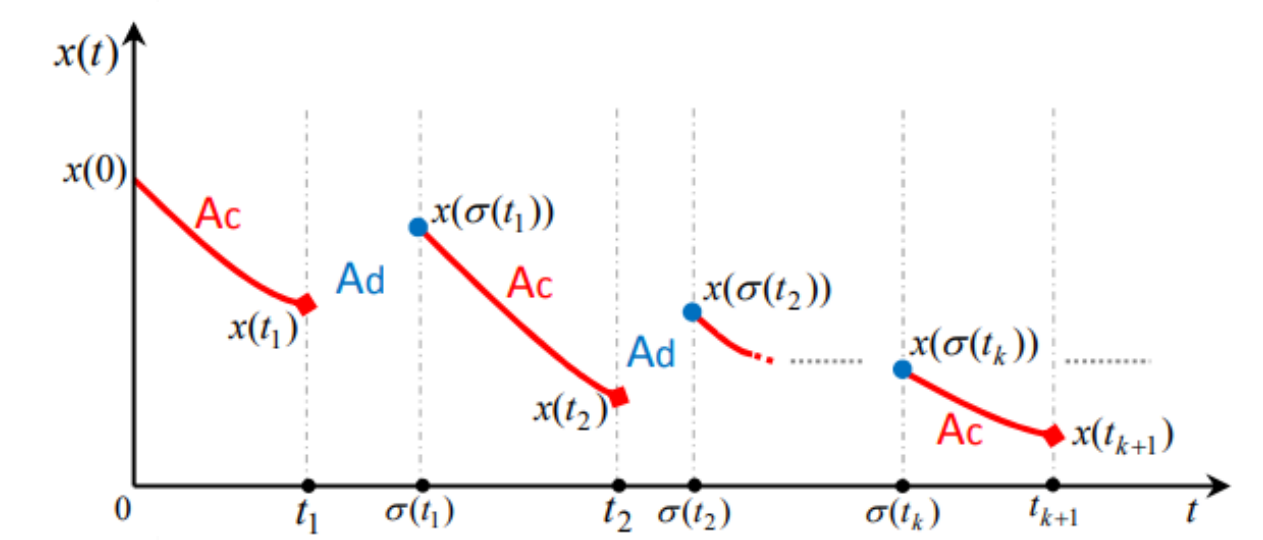
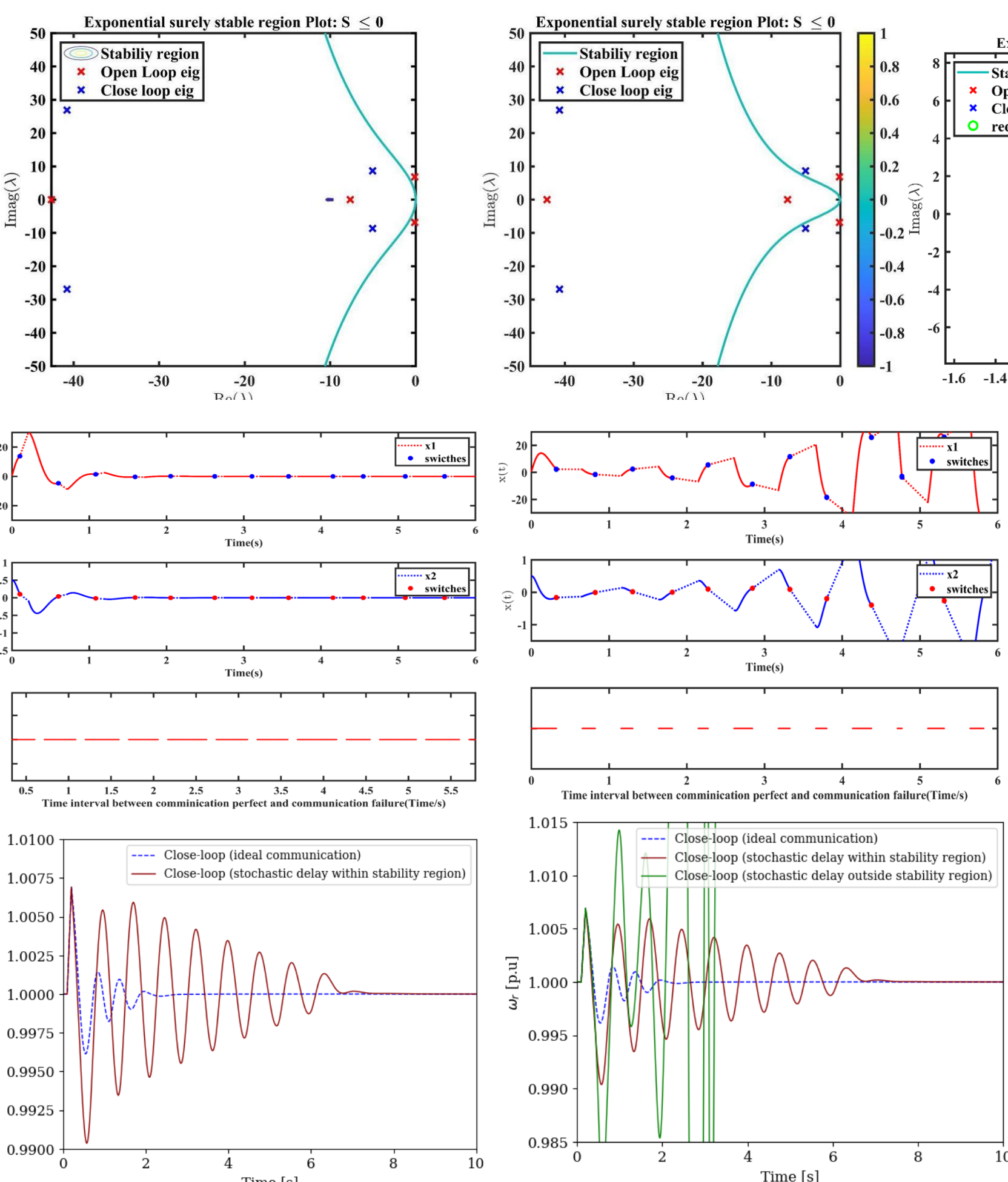
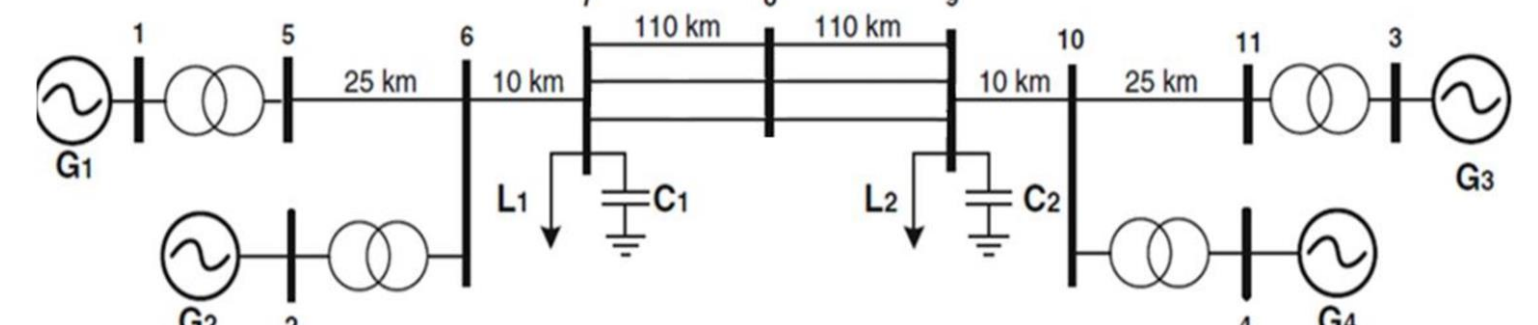
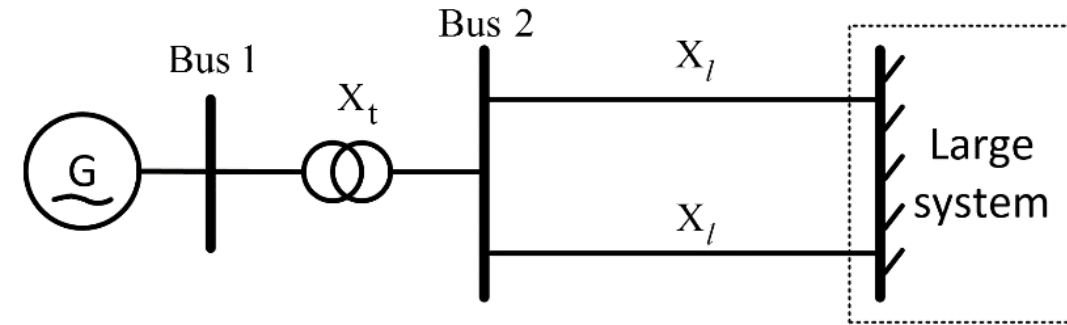


Fig. 2 switched system on non-uniform time domain

3 CASE STUDY

- Study systems: SMIB/Kundur Two Area



- Region of exponential stability almost surely by following different distributions.
- State trajectories in case of stochastic time delay.
- Maximum allowable time delay: Case1(0.3246s), Case2(1.2635s)
- Dwell time delay: Case1(0.21s), Case2(0.61s)

